

FIG. 1A

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Pin No.	PCI Express card	USB card	Description
1	GND	GND	Ground
2	USB_D-	USB_D-	USB data
3	USB_D+	USB_D+	USB data
4	CPUSB#	CPUSB#	USB card detect and power control
5	RESERVED	RESERVED	Reserved
6	RESERVED	RESERVED	Reserved
7	RESERVED	RESERVED	Reserved
8	SMB_CLK	SMB_CLK	SMBus clock
9	SMB_DATA	SMB_DATA	SMBus data
10	+1.5V	+1.5V	Supply (+1.5V)
11	WAKEB	WAKEB	Notify host of wake-from-sleep mode
12	+3.3VALK	+3.3VALK	Supply (+3.3V)
13	PERST#	PERST#	PCI Express function reset
14	+3.3V	+3.3V	Supply (+3.3V)
15	+3.3V	+3.3V	Supply (+3.3V)
16	CLKREQ#	CLKREQ#	Used when REFCLK required with PCI Express
17	CPPEA	CPPEA	PCI Express card detect and power control
18	REFCLK-	REFCLK-	Clock signal reference
19	REFCLK+	REFCLK+	Clock signal reference
20	GND	GND	Ground
21	PERn0	PERn0	PCI Express data receive
22	PERp0	PERp0	PCI Express data receive
23	GND	GND	Ground
24	PERn0	PERn0	PCI Express data send
25	PERp0	PERp0	PCI Express data send
26	GND	GND	Ground
	Long pin	Unused pin	

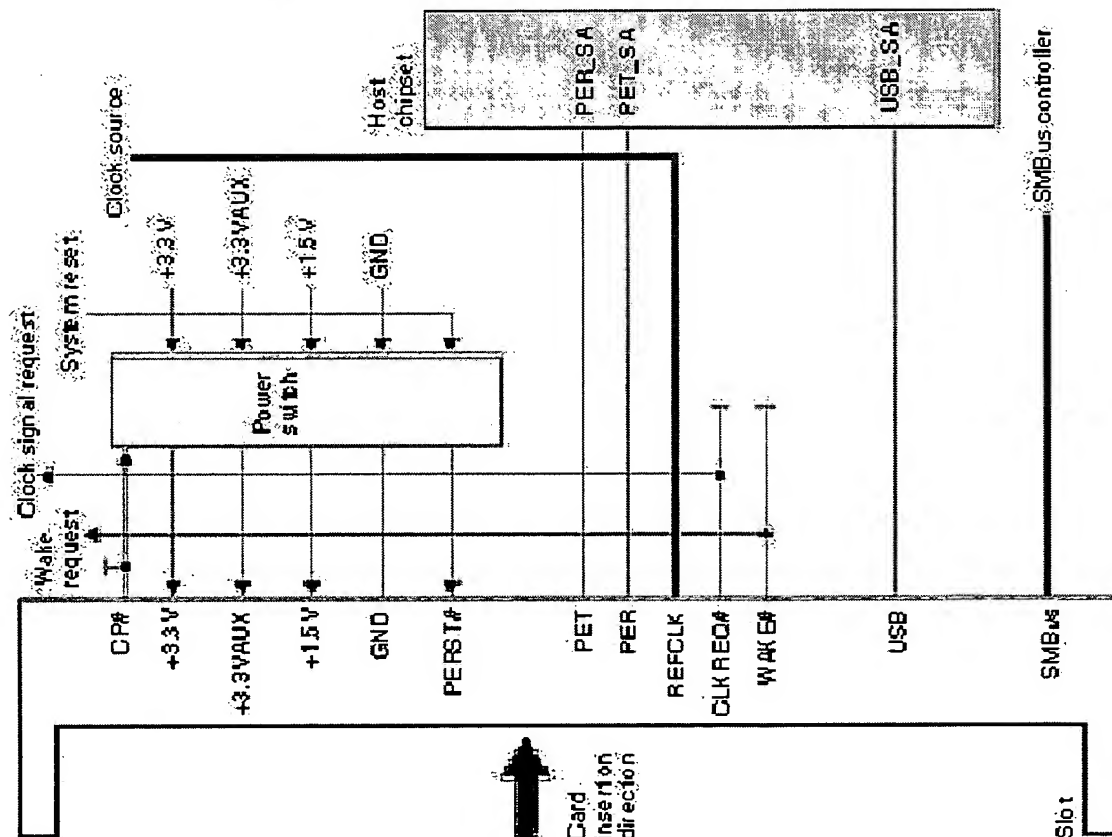


FIG. 1B (PRIOR ART)

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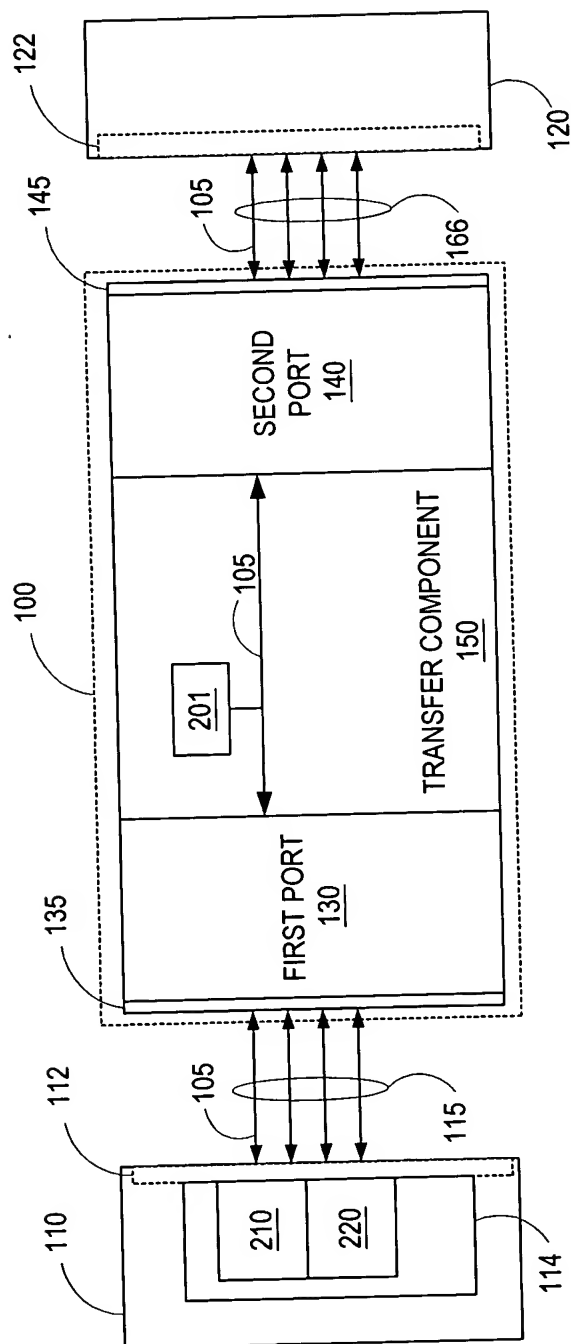


FIG. 2

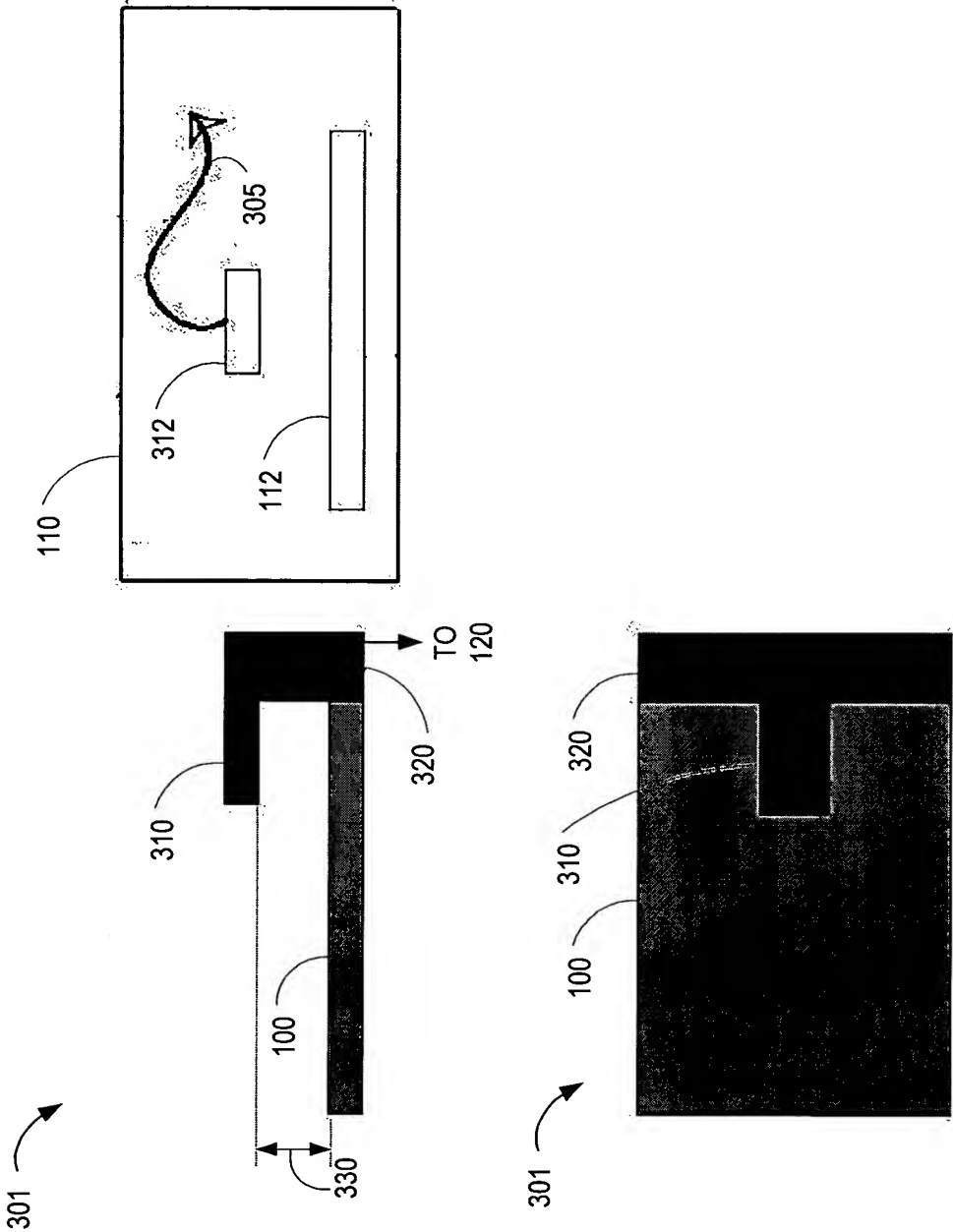
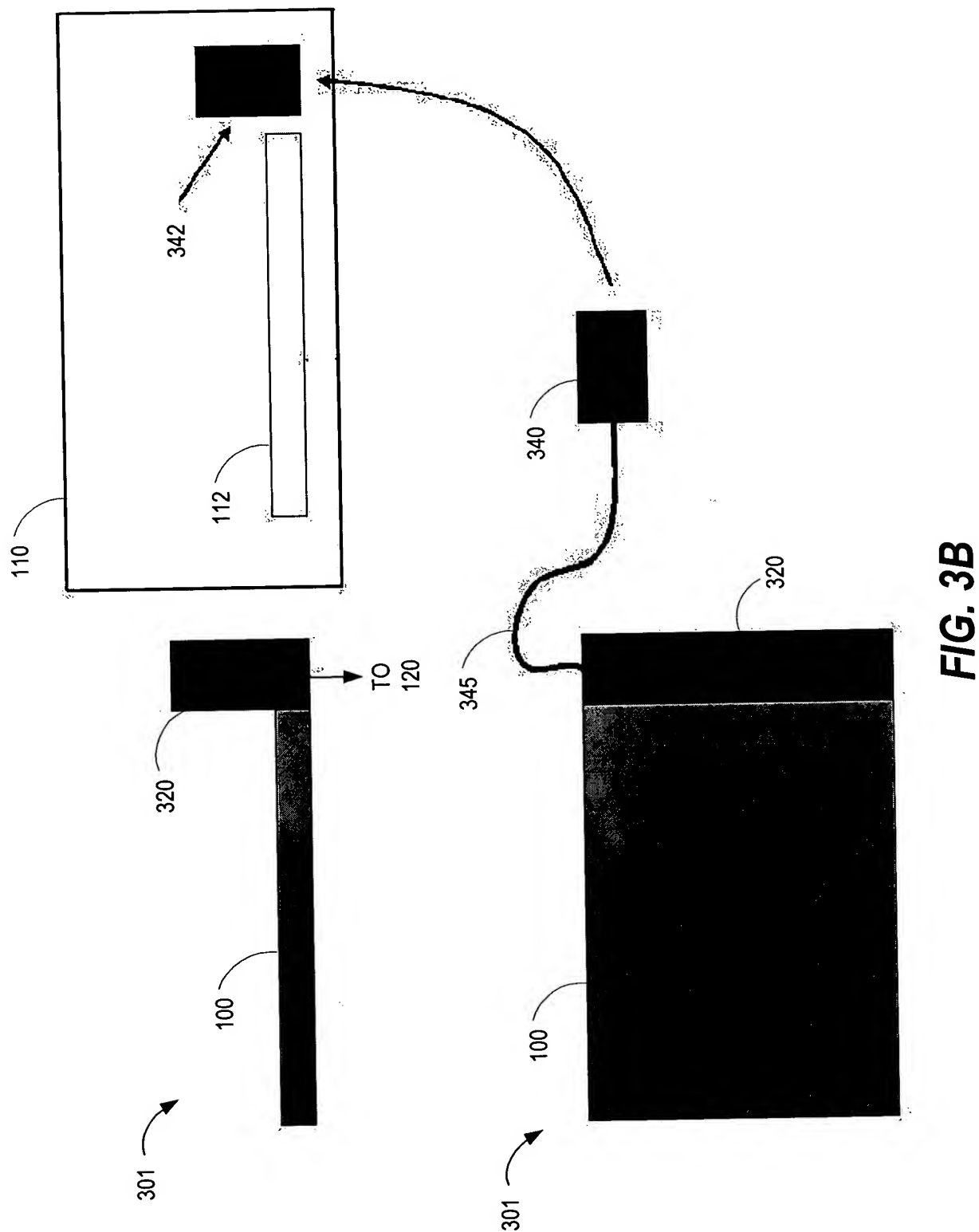


FIG. 3A

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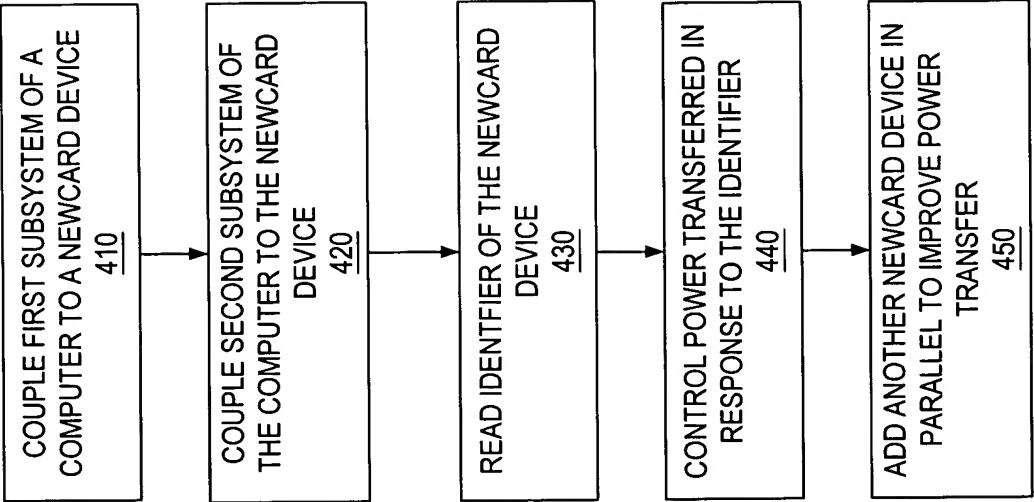


FIG. 4

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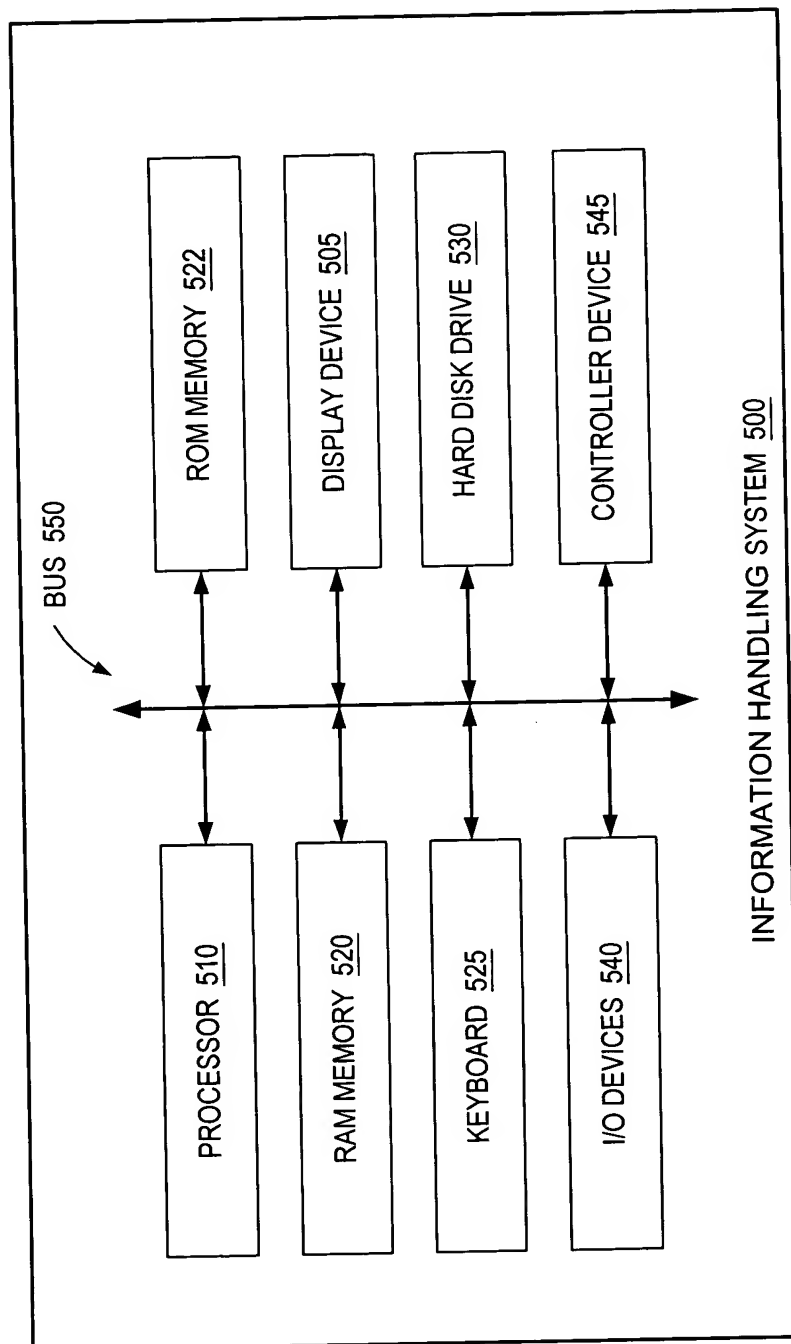


FIG. 5